

**Notice of References Cited**

Application/Control No.

10/662,034

Applicant(s)/Patent Under  
Reexamination  
MANTEY ET AL.

Examiner

MATTHEW D. SPITTLE

Art Unit

2111

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
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**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Zitouni et al. Design of an Asynchronous VME bus Controller for Heterogeneous Systems. Dedicated Systems Magazine. 2000.
	V	Ikezaki et al. Design of Home Bus Controller and Its Application Based on HBS Standard. IEEE Transactions on Consumer Electronics. Vol. 34, No. 3, August 1988.
	W	Advanced Micro Devices, Inc. AMD-766 Peripheral Bus Controller Data Sheet. March 2001.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.